

SDIO 3.0 to UART Bridge Resource Utilization

Xilinx:

Device Family	LUTs	Registers	BRAM
ZYNQ Ultrascale +	1114	1183	2
ZYNQ 7000	1114	1252	2

Intel:

Device Family	ALM	Registers	RAM Blocks
Stratix V	940	1320	2
Cyclone V	945	1350	2
Arria 10	959	1316	2

Lattice:

Device Family	LUT4s	Registers	Block RAMs
ECP5U	2305	1143	2

Microchip:

Device Family	4LUT	DFF	RAM1K20/ RAM1K18
Smart fusion 2	2076	1205	2
Polar fire	2015	1157	2