

## SATA Bridge Controller Resource Utilization

Xilinx:

Device Family	LUTs	Registers	BRAM
ZYNQ Ultrascale +	4062	3640	26
ZYNQ 7	3901	3781	26

Intel:

Device Family	ALM	Registers	RAM Blocks
Stratix V	4856	3505	26
Cyclone V	4851	3470	26
Arria 10	4895	3553	26