

8254 Timer Resource Utilization

Xilinx:

Device Family	LUTs	Registers
Spartan 6	334	315

Lattice:

Device Family	LUTs	Registers
ECP3A	689	323

Intel:

Device Family	Logical elements Combinational	Logical Elements registers
Cyclone IV E	659	315

Microchip:

Device Family	Core SEQ	Core COMB
ProASIC3	315	875