

Video Decoder Interface Resource Utilization

Xilinx:

| Xilinx Device | Slice Registers | Slice LUTs | RAMB8BWERS |
|---------------|-----------------|------------|------------|
| Spartan 6 | 368 | 254 | 1 |

Lattice:

| Lattice Device | Slice Registers | Slice LUTs |
|----------------|-----------------|------------|
| XP2 | 405 | 319 |

Altera:

| Altera Device | Logic Elements Combinational | Logic Elements Registers | M9K Blocks |
|---------------|---------------------------------|-----------------------------|------------|
| Cyclone IV E | 297 | 374 | 1 |

Actel:

| Actel Device | Core SEQ | Core COMB | RAM/FIFO |
|--------------|----------|-----------|----------|
| ProASIC3 | 405 | 476 | 1 |