

8254 Programmable Timer Resource Utilization

Xilinx:

Xilinx Device	Slice Registers	Slice LUTs
Spartan 6	315	334

Lattice:

Lattice Device	Slice Registers	Slice LUTs
ECP3A	323	689

Altera:

Altera Device	Logic Elements Combinational	Logic Elements Registers
Cyclone IV E	659	315

Actel:

Actel Device	Core SEQ	Core COMB
ProASIC3	315	875