

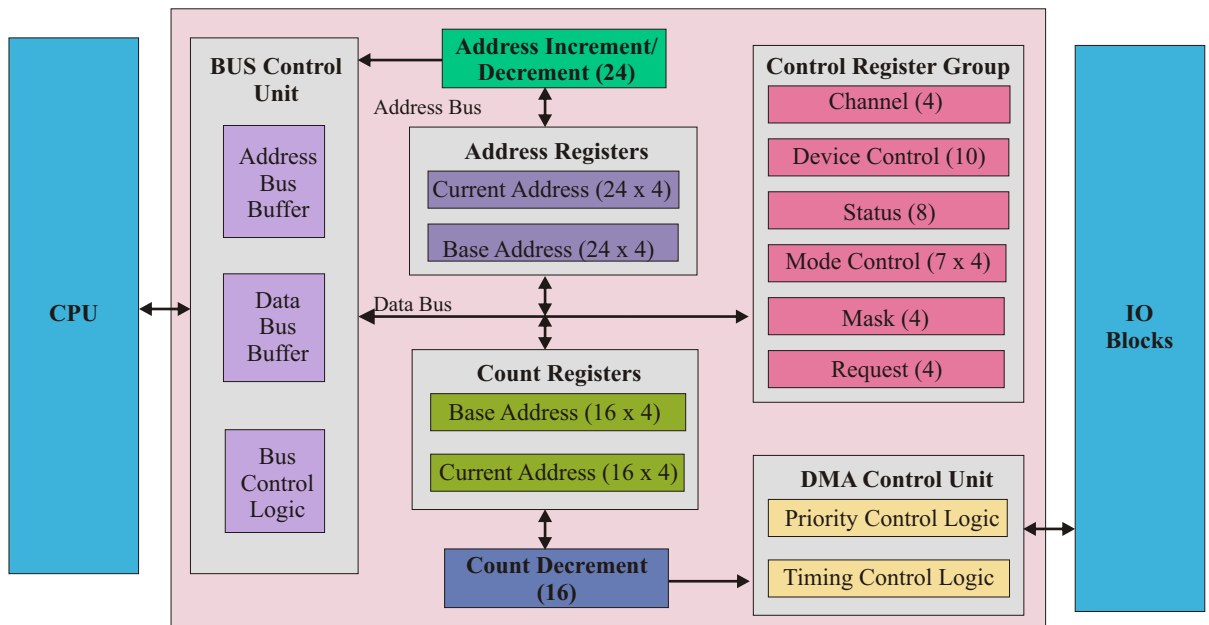
Overview

iW-DMA Controller is a peripheral core for microprocessor systems. It controls data transfer between the main memory and the external systems with limited CPU intervention.

Features

- ❖ Compatible to 8237
- ❖ 24-bit length address register
- ❖ 16-bit length count register
- ❖ 4 independent DMA channels
- ❖ 4 clock / 1 bus cycle
- ❖ Byte transfer / word transfer selectable
- ❖ Supports three transfer modes (Single, Demand, Block)
- ❖ Two bus modes (Bus release, Bus hold)
- ❖ DMA request maskable on an individual channel basis
- ❖ Software DMA request in uPD1037 mode
- ❖ Auto initialization function
- ❖ Two kinds of channel priority order (Fixed & Rotating)
- ❖ Cascading capability

Block Diagram



Core Benefits

- ❖ Uses to reduce the load of the CPU
- ❖ Configurable for ease of use

Deliverables

- ❖ Technical Specification
- ❖ RTL Verilog Synthesizable Code
- ❖ Comprehensive Test Environment
- ❖ Technical Support and Maintenance

About Us

iWave Systems Technologies is an embedded Hardware and Software Turnkey Design Services focused on providing integrated solutions for developing innovative products and systems in the areas of Communication, Consumer Electronics and Multimedia. iWave offers complete turnkey solutions for systems engineering and product development.

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