

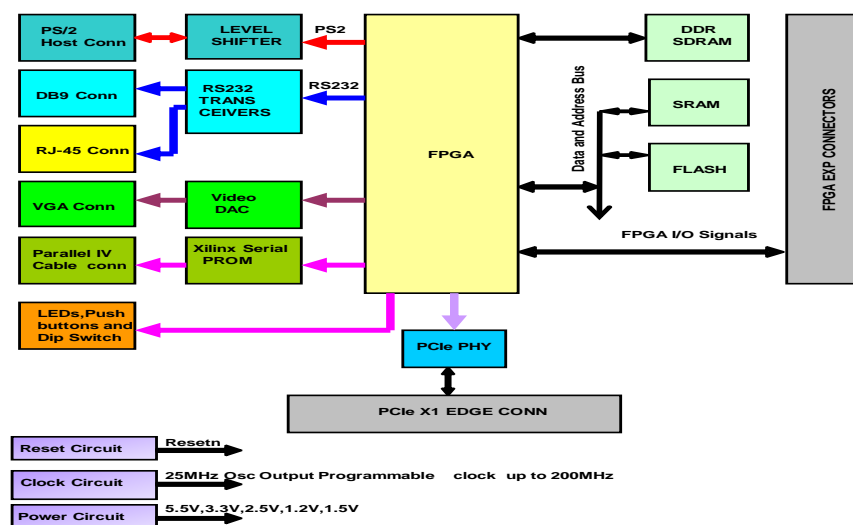
## Overview

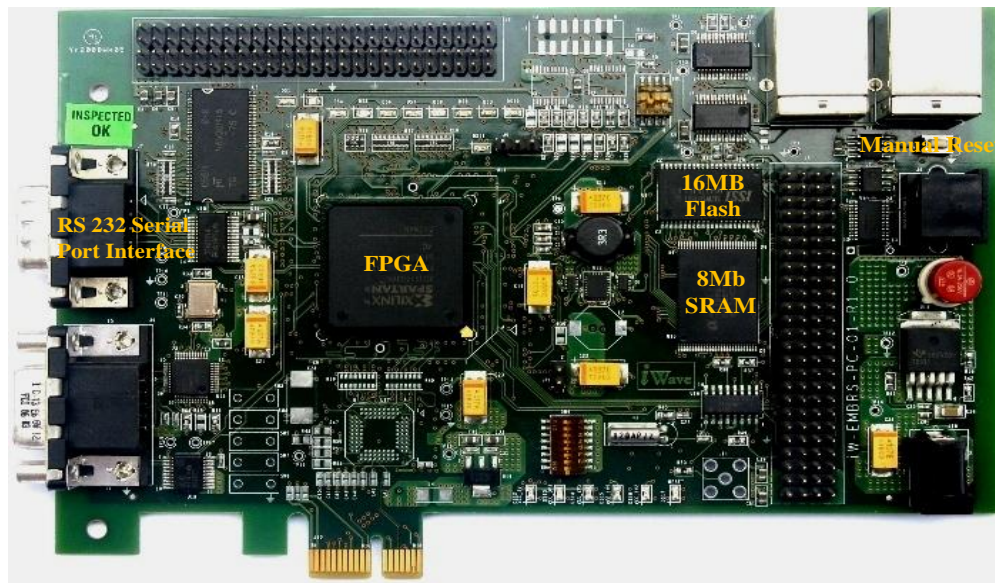
iW-186 Evaluation Board is mainly developed for demonstration of the iW-80186 processor IP. Using this board user can do different diagnostic tests which can verify the proper functioning of iW-80186 processor. The rich supporting interfaces available in the board, also gives the benefit of demonstrating the user FPGA designs with necessary customization.

## Features

- FPGA
  - SPARTAN-3AN FPGA (XC3S1400AN)
- Memory
  - DDR SDRAM (supports up to 1Gb)
  - SRAM (8Mb)
  - Flash (supports up to 16MB)
- PCI Express Bus Interface
  - TI XIO1100 2.5Gbs PCI Express Single Lane PHY
- Communication Ports
  - Three Serial Port Interface through RS232 Transceivers
  - PS/2 Host
- Video
  - RBG Video DAC and VGA Display Port
- Power
  - TPS75933 Regulator for 3.3V@7.5A generation
  - Triple Supply power management IC for 1.2V@3A, 2.5V@3A, optional voltage @300mA
  - LT1963-1.5 for 1.5V@1.5A generation
- Configuration
  - Xilinx XCF04S PROM connected in parallel for SPARTAN-3AN FPGA
  - Parallel IV Cable connector for SPARTAN-3AN FPGA
- FPGA I/O signals through Expansion Connectors.

## Block Diagram





### **Deliverables**

- iW-186 Evaluation Board
- Schematics of the board
- Board user manual
- Power supply (1 no.)
- Serial cable (1 no.)