

Serial FPDP Resource Utilization

Xilinx:

Xilinx Device	Slice Registers	Slice LUTs	Number of GTP Transceivers	Number of PLLs	Number of Block RAMs
Virtex-5	660	550	1	1	2

Lattice:

Lattice Device	Slice Registers	Slice LUTs	Number of Memory Blocks	Number of SERDES	Number of PLLs
ECP3	660	740	4	1	1

Altera:

Altera Device	Combinational ALUTs	Logic Elements Registers	Number of Transceivers	Number of PLLs	Number of M9Ks
Stratix IV	660	550	1	1	8