

## SDIO to UART Bridge Resource Utilization

### Xilinx :

Xilinx Device	Slice Registers	Slice LUTs	RAMB16BWER	RAMB8BWER
Spartan 3E	1115	1583	2	0

### Altera:

Altera Device	Logic Elements Combinational	Logic Elements Registers	Memory Bits	No of Pins
Cyclone IV E	1,902	1,157	33,024	16

### Actel:

Actel Device	Core SEQ	Core COMB	RAM/FIFO	Number of IOs
ProASIC3	1349	3055	4	16