

PCIe to SD/MMC Bridge Resource Utilization

Xilinx :

Xilinx Device	Slice Registers	Slice LUTs	RAMB16BWER	RAMB8BWER	DCMs
Spartan 6	2429	2114	4	1	1

Altera:

Altera Device	Logic Elements Combinational	Logic Elements Registers	Memory Bits	PLL
Arria II GX	1,850	2,473	71168	1