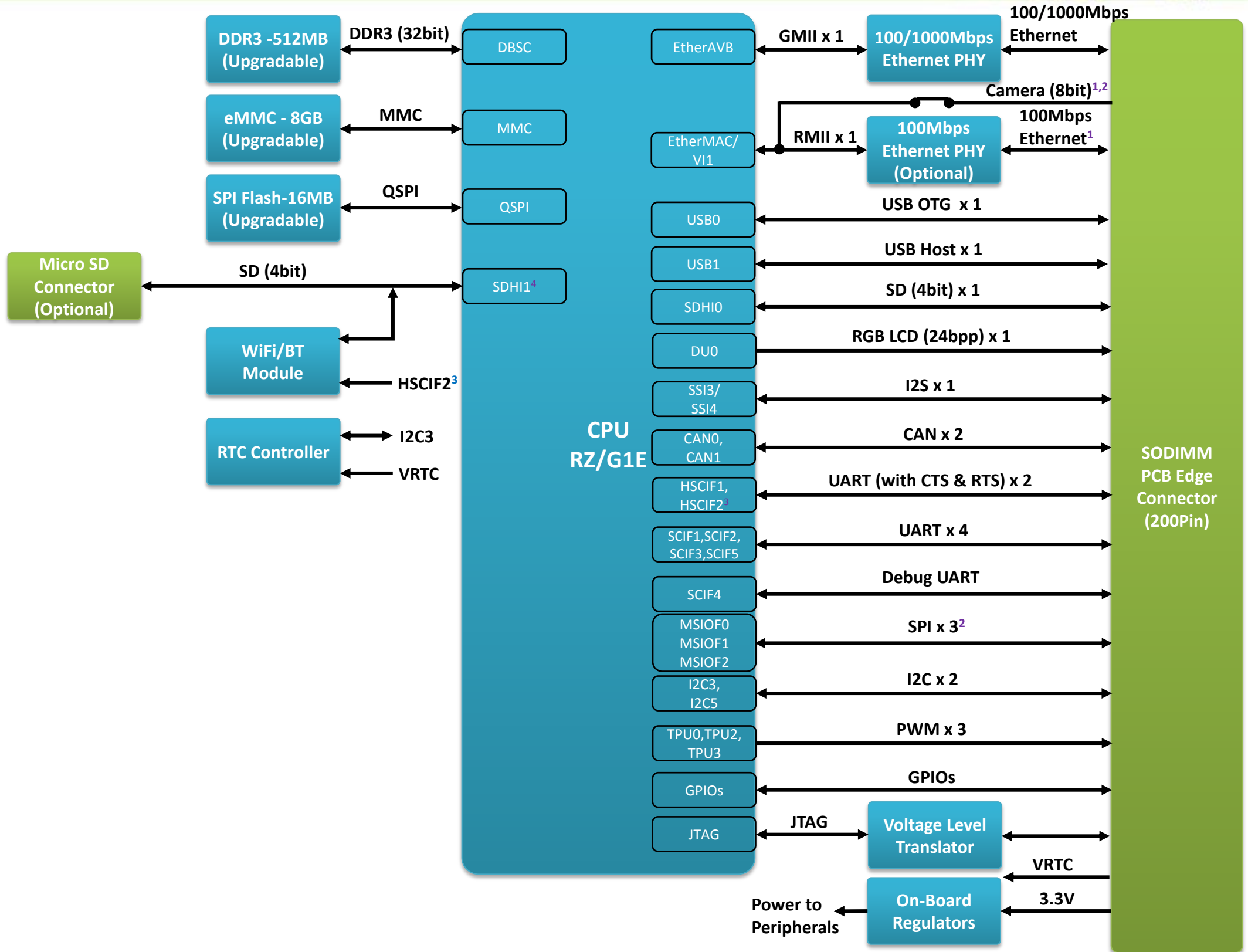


iW-RainboW-G22M - RZ/G1E SODIMM SOM Block Diagram



¹ Since EtherMAC and VI1 are multiplexed in CPU, either one only can be used at a time. By default VI1 is supported.

² Since VI1 and MSIOF1 are shared the same SODIMM Edge pins, either one only can be used at a time. By default VI1 is supported.

³ Since HSCIF2 interface is shared between Wi-Fi/BT and SODIMM Edge pins, either one only can be used at a time. By default Wi-Fi/BT module is supported.

⁴ Since SDHI1 interface is shared between Wi-Fi/BT module and μ SD Connector, either one only can be used at a time. By default Wi-Fi/BT module is supported.