

### Device Utilization Summary

IP	Xilinx Device	Fmax (MHz)	Slices	4 input LUTs	Gate count	IOB	GCLK	BRAM	MULT / DSP48	DCM	Power (mW)	Design Tool
x86 Processor	XC3S1500	40	6823 (51%)	11542 (43%)	125833	115 (35%)	8 (100%)	0	2 (6%)	0	519	ISE 9.2.03i

This includes the following cores

1. CPU core
2. 8254 equivalent timer
3. 8259 equivalent interrupt controller
4. 8237 equivalent DMA controller
5. 8251 equivalent serial controller

Area consumption of individual cores is listed below

IP	Example Device	Slices	4 input LUTs	Gate count	BRAM	MULT/ DSP48	DCM	Design Tool
CPU core	XC3S1500	3982 (33%)	7203 (27%)	77448	0	2 (6%)	0	ISE 9.2.03i
8254 equivalent timer	XC3S1500	340 (3%)	517 (2%)	5507	0	0	0	ISE 9.2.03i
8259 equivalent interrupt controller	XC3S1500	296 (2%)	456 (2%)	4237	0	0	0	ISE 9.2.03i
8237 equivalent DMA controller	XC3S1500	1719 (13%)	2730 (10%)	27437	0	0	0	ISE 9.2.03i
8251 equivalent serial controller	XC3S1500	121 (1%)	187 (1%)	2547	0	0	0	ISE 9.2.03i
Miscellaneous Peripherals	XC3S1500	365 (3%)	449 (2%)	8657	0	0	0	ISE 9.2.03i

#### Additional Peripherals

IP	Example Device	Slices	4 input LUTs	Gate count	BRAM	MULT/ DSP48	DCM	Design Tool
8530 equivalent Multi protocol serial controller	XC3S1500	3364 (25%)	5381 (20%)	52985	0	0	0	ISE 9.2.03i
PCI target controller	XC3S1500	125 (1%)	155 (1%)	2616	0	0	0	ISE 9.2.03i

**Note :**

IOB – Input / Output Block

BRAM – Block RAM

DCM – Digital Clock Manager

GCLK – Global Clock

MULT – Multiplier